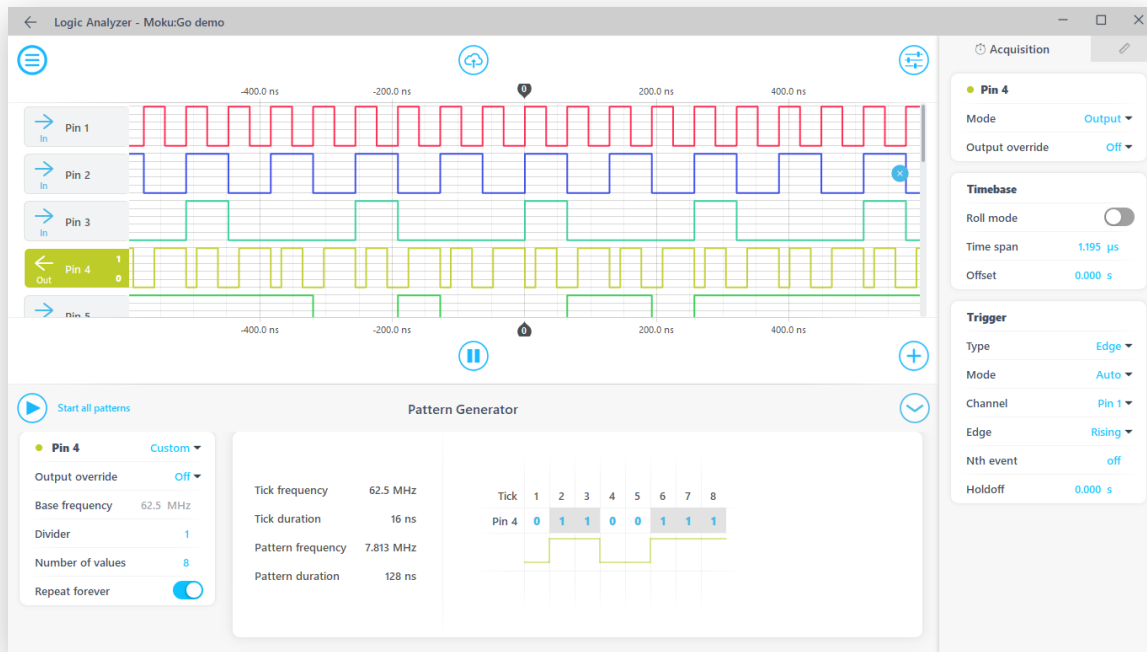




Moku:Go's Logic Analyzer / Pattern Generator is equipped with 16 bidirectional digital I/O with sampling rates up to 62.5 MSa/s. It supports 3.3 V logic levels (5V tolerant) and 1M ×16 input sample depth. Measurements are readily available through the interface. Data, screenshots can be captured and uploaded to the computer. Combined with the analog inputs and outputs, and the intuitive graphical user interface, Moku:Go is your go-to solution for undergraduate lab curriculum lab and senior design project.



Number of DIO 16	Sampling Rate 62.5 MSa/s	Maximum Clock Frequency 31.25 MHz	Supported Logic Level 3.3 V, 5 V tolerant	Input Memory Depth 1M × 16	Supported Math AND, OR, XOR, etc
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Features

- 16 channel bidirectional digital I/O with sampling rates up to 125 MSa/s.
- Support 3.3 V logic level, with 5 V tolerance for the inputs.
- Ultra deep 1M × 16 points input memory depth, 1024 × 16 points output memory depth (with 131k × 16 coming later this year).
- Powerful, intuitive graphical user interface with Python, and MATLAB API support.

Specifications

- Number of I/O: 16 bidirectional
 - Input memory depth: 1M × 16
 - Output memory depth: 1024 × 16
- Input**
- Logic level: 3.3 V, 5 V tolerant
 - Impedance: >10 MΩ, <4 pF
 - Sampling rate: up to 62.5 MSa/s
 - Maximum clock frequency: 31.25 MHz
- Output**
- Logic level: 3.3 V
 - Impedance: 400 Ω, <4 pF
 - Sampling rate: up to 62.5 MSa/s
 - Maximum clock frequency: 31.25 MHz
- Supported Math: AND, OR, XOR, NAND, NOR, XNOR

Applications

- Signal simulation
- Digital circuit design
- Digital communication diagnosis
- Clock source